**Weekly Project Report (Week 5 – 10/29)**

1. **Last Week:**

* Started to make schematic
* Attempted to make two boards talk together using SPI.
* Shifting from SPI to UART.
* Brainstormed layout.

1. **Next Week:**

* Attempt to complete schematic
* Continue programming on two boards
* Acknowledge datasheet to use UART.
* Start to make layout**.**

1. **Issue:**

* Unable to program two boards talk together , so we try to figure out how to make master module to read data from slave module